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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 09/665,034 09/19/2000
which is a CON of 09/153,630 09/15/1998 PAT 6,153,829

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 12/03/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY AZ	SHEETS DRAWING 3	TOTAL CLAIMS 24	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged Examiner's Signature: <i>[Signature]</i> Initials: <i>[Initials]</i>			

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TITLE

Methods of split cavity wall plating for an integrated circuit package

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